

**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Junichi KARASAWA et al.

Application No.: 10/807,288

Docket No.: 119222

Filed: March 24, 2004

For: FERROELECTRIC LAYER, METHOD OF MANUFACTURING  
FERROELECTRIC LAYER, FERROELECTRIC CAPACITOR, AND  
FERROELECTRIC MEMORY

**REQUEST FOR CORRECTION OF PALM RECORDS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,

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JAO:TJP/emt

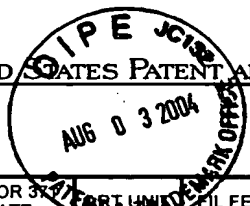
Date: August 3, 2004

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**P.O. Box 19928**  
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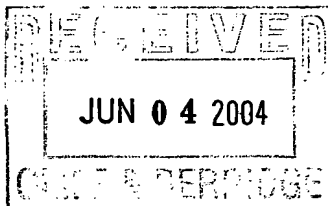
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APPL NO.	FILING OR 35 U.S.C. 111(a) DATE	PARTIAL FILING DATE	FILED FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND-CLMS
10/807,288	03/24/2004	2871	770	119222	3	18	3

25944  
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CONFIRMATION NO. 3917

FILING RECEIPT



\*OC000000012852973\*

Date Mailed: 06/03/2004

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Applicant(s)

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Assignment For Published Patent Application

SEIKO EPSON CORPORATION, Tokyo, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2003-088219 03/27/2003

If Required, Foreign Filing License Granted: 06/02/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

**Title**

Ferroelectric layer, method of manufacturing ferroelectric layer, ferroelectric capacitor, and ferroelectric memory

**Preliminary Class**

349

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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